



Revealing the influence of edge states on the electronic properties of PtSe₂

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ABSTRACT

The properties and performance of 2D nanostructures are known to be critically affected by localized edge states. Here, platinum diselenide (PtSe₂) films, synthesized by thermally assisted conversion, are characterized by a variety of spectroscopic and microscopic methods, including scanning tunneling microscopy and spectroscopy, and scanning transmission electron microscopy. We report the presence of distinct edge states, and use surface Green's function methodology in atomistic modeling of different edge atomic structures, together with other theoretical tools, to investigate their origin. Our results indicate that the edges, inherent and abundant in polycrystalline films, can be semiconducting or semimetallic in nature depending on the edge configuration. Experimental results in conjunction with first-principles calculations demonstrate a mapped band profile of the atomically-sharp step edges of PtSe₂, which form lateral heterojunctions possessing large asymmetric band offsets in the conduction and valence bands. We further explore the viability of a single step monomaterial heterojunction based field-effect transistor, through atomistic quantum transport simulations. This reveals the critical role of the edge states in creating a wider energy band for carrier transport in the ON-state and hence enabling a remarkably large I_{ON}/I_{OFF} ratio > 10¹⁰ and near-ideal subthreshold slope, fulfilling the requirements for low-power applications.

1. Introduction

Owing to their wealth of unique and interesting properties, the value and potential of two-dimensional (2D) layered materials are undisputedly recognized. 2D materials evidently have the potential to revolutionize the fields of electronics, energy, and catalysis [1–7]. Critically, their exceptional properties are often not apparent in the bulk forms of the base materials. As such, methods that can produce 2D materials with controllable well-defined qualities and dimensions are essential. The morphologies of 2D materials are highly dependent on the synthesis processes and conditions. High-quality materials can be obtained by

mechanical exfoliation of bulk crystals and molecular beam epitaxy (MBE), but these methods are not generally regarded as being scalable. Chemical vapor deposition (CVD) is an established route for the production of large-area, high quality films down to monolayer thickness; however, it often requires high temperatures. Other approaches such as liquid phase exfoliation (LPE) and thermally assisted conversion (TAC) can be expedient, due to their simplicity and in many cases lower thermal budget. Furthermore, the polycrystalline films produced by these methods can be highly advantageous for applications that rely on edge sites, such as catalysis and sensing [8,9]. However, understanding and characterizing the electronic features of polycrystalline films is

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more challenging than simple idealized structures.

As 2D materials are adopted for use and move to the forefront of materials science research, it has never been more important to understand the nature of inherent defects that modify their intrinsic properties. Due to the dimensional confinement, defects proportionally play a significant role in the electronic and atomic structure of 2D materials [10,11]. Indeed, the extraordinary responsiveness of sensors based on 2D materials can be attributed to the appreciable influence even single defects and adsorbed molecules can have [12,13]. Numerous studies have investigated the presence and influence of point defects (0D) in transition metal dichalcogenides (TMDs) [14–17]. However, for sheets with restricted lateral dimensions, 1D defects in the form of crystal edges may be the dominant defect type present. The edges of 2D materials can feature a variety of termination states including dangling bonds and edge reconstruction. These can have properties distinct from those of the 2D planes, give rise to new phenomena, and introduce additional functionalities [18–22]. It is notable that the fabrication of flexible and portable electronics and energy-storage devices by methods such as direct-ink writing of functional materials characteristically employ polycrystalline liquid exfoliated materials [23,24], whose edge states have been observed to host distinct properties [25,26]. Understanding

the influence of edges becomes more critical as the thickness of the crystallites within films is reduced, as this is typically correlated with the lateral dimensions [27,28].

Since their initial identification, group-6 TMDs have been the dominant 2D semiconducting materials of choice. However, more recently group-10 TMDs, also referred to as noble-metal TMDs (NTMDs), have come to prominence due to their suitability for future nanoelectronics. While the extensively studied group-6 TMDs (e.g., MoS₂, MoSe₂) commonly adopt the 2H phase, the NTMDs (e.g., PtSe₂, PdS₂) distinguish themselves with a broader variety orderings, for example the 1T phase is typically observed for PtSe₂, as shown in Fig. 1(a), and PdS₂ adopts the 2O phase [29]. A member of the NTMDs that has lately drawn considerable interest is PtSe₂, which displays a wealth of interesting properties [30,31]. In its bulk-like state, PtSe₂ exhibits semi-metallic behavior, but its electronic structure can be modulated by controlling the layer number. It was predicted and confirmed to undergo a semimetal-to-semiconductor transition with reduced layer numbers [32–34]. Single- and few-layer PtSe₂ are typically, though not exclusively, observed to be *p*-type semiconductors with a tunable bandgap [4, 32]. It has been predicted that monolayer PtSe₂ undergoes an indirect-bandgap to a direct-bandgap semiconductor transition for a

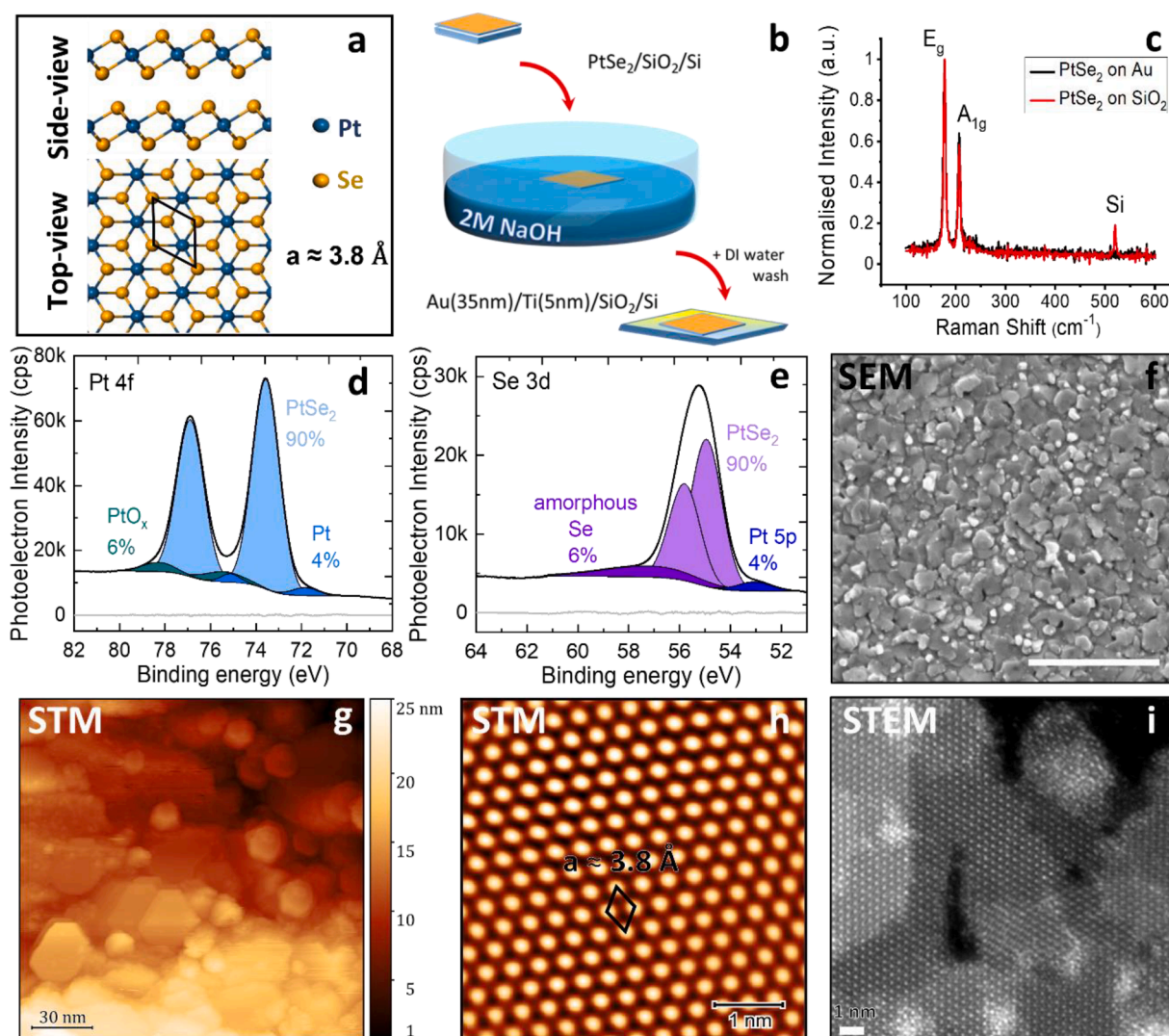


Fig. 1. Polycrystalline PtSe₂ films. (a) Schematic view of 1T-PtSe₂ crystal structure. (b) Schematic of the lift-off and transfer process. (c) Raman spectra of PtSe₂ film before and after transfer, showing characteristic PtSe₂ peaks and an additional Si peak at 520 cm⁻¹ from the original substrate. XPS of PtSe₂ film showing the Pt 4f (d) and Se 3d (e) core levels. (f) SEM image of transferred PtSe₂ film (scale bar is 500 nm). (g) STM image of a PtSe₂ film displaying crystals (170 × 170 nm, V = 1.2 V and I = 34 pA). (h) Atomic-resolution STM image in a defect-free area with an interatomic distance of 3.8 Å (5 × 5 nm², V = 0.2 V, I = 650 pA). (i) HAADF-STEM image of multilayer 1T-PtSe₂ (scale bar is 1 nm).

strain of 8% [35]. Notably it exhibits high charge-carrier mobility, with experimental values ranging to $\sim 625 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, [36] and a theoretical mobility comparable to that of black phosphorus, [37,38] but with the benefit of much greater in-air stability. These remarkable semi-conducting properties come in conjunction with other notable electronic properties. Studies have suggested its suitability for electrically-tunable spintronic applications, [39,40] and with strain playing an important role in its performance, PtSe₂ is proposed to have one of the highest Seebeck coefficients among the TMDs [41,42]. In addition, the relatively low synthesis temperature of PtSe₂ by TAC enables back-end-of-line (BEOL) integration and the growth of the material directly on polymer substrates for strain gauges [43,44]. In light of these beneficial properties, PtSe₂ has demonstrated potential for application in areas such as transistor electronics, [32,45,46] broad spectrum photo-detection [47, 48], catalysis [8,49,50], and gas sensing [31]. Quality is an important factor, it has been reported that defects can induce magnetic ordering in PtSe₂ [10,51]. Furthermore, variations of crystallite size in polycrystalline films have been reported to influence the PtSe₂ film properties, e.g. mobility, suggesting edge density might also be a contributing factor [52]. Indeed, specifically related to crystal edges, it has been shown that the density of edge sites can be controlled during synthesis [8], which could be beneficial for catalysing the hydrogen evolution reaction [53].

The properties of crystal edges are widely relevant to 2D materials, moreover, PtSe₂ presents an inherently interesting system to study such edge effects. It has been suggested that energy profiles at the step edges is qualitatively different depending on the strength of the interlayer interaction, which is a consideration for materials with stronger interlayer interactions such as PtSe₂ [19]. From an application viewpoint, self-contacting devices are possible due to the semimetal-to-semiconductor transition with reduced thickness, and such geometries will necessarily possess such edges [54]. Furthermore, it is anticipated that the strong quantum confinement effect in PtSe₂ results in a rather large band offset in the conduction band and/or valence band due to the thickness variation at the edge, creating a monomaterial heterojunction. Vertical stacking of layered materials to create heterojunctions is a subject of ongoing research, but given the challenges associated with the growth of the dissimilar materials, monomaterial heterojunctions with atomically-sharp interfaces and uniform chemical composition offer an opportunity to engineer more ideal Schottky barriers and novel device concepts for nanoscale field-effect transistors (FETs) [55–57].

Step edges are a dominant feature within polycrystalline films, therefore insights into the electronic states such features are valuable for the application of such films in devices or for catalysis, and also ultimately for self-contacting architectures. Using complementary techniques, here we investigate and discuss the electronic nature of the step edges on PtSe₂. Polycrystalline PtSe₂ is a practical system that offers plentiful step and edge features to experimentally examine. Low-temperature scanning tunneling microscopy (STM) and scanning transmission electron microscopy (STEM) provide atomic-resolution images of the crystallites within the polycrystalline films, while scanning tunneling spectroscopy (STS) is used to map the local density of states (LDoS). High-angle annular dark-field (HAADF) STEM imaging is used for elemental identification of isolated atoms in crystallites, through its Z-contrast property, which enables understanding of the edge terminations of PtSe₂ crystals. In order to understand the band alignment and explore the possible applications of the monomaterial heterojunction, we systematically study the electronic properties of different edge terminations in monolayer (ML) and bilayer (BL) PtSe₂ separately, in addition to those in a ML-BL PtSe₂ heterojunction, using first-principles calculations.

2. Results

PtSe₂ was synthesized through the TAC of Pt films [31]. Pt thin films,

with thicknesses up to 5 nm, were deposited on SiO₂/Si substrates by argon-ion sputtering. The 1T atomic structure of the PtSe₂ is shown in Fig. 1(a). PtSe₂ was obtained by annealing the samples at 450 °C for two hours under a 150 sccm H₂/Ar flow in the presence of an upstream-heated elemental Se source. As a conductive substrate is required for electrical measurements, e.g. STM/STS, the PtSe₂ films were liberated from their insulating growth substrates by etching in 2 M NaOH solution and floated onto Au/Ti (30 nm/5 nm) on SiO₂/Si substrates, as illustrated in Fig. 1(b) and further described in the experimental section. The conversion process yields matted polycrystalline films, and the morphology at each stage, before and after transfer, was verified by atomic force microscopy (AFM), Fig. S11. Raman spectroscopy and X-ray photoemission spectroscopy (XPS), Fig. 1(c–e), were used to assess and confirm the synthesis of the PtSe₂ films. Raman spectra of as-grown and transferred films, shown in Fig. 1(c), display the characteristic E_g and A_{1g} modes verifying the successful synthesis and transfer of PtSe₂ [58]. XPS was conducted to investigate the compositional quality of the PtSe₂ films. All energy positions were corrected with respect to the adventitious carbon peak at 284.8 eV. Fig. 1(d, e) demonstrate the spectra of the Pt 4f and Se 3d core-level regions of a representative film (5 nm thick starting Pt film) of PtSe₂. The Pt 4f core-level spectrum is deconvoluted into three contributions: the main doublet peak at $\sim 73.5 \text{ eV}$ (4f 7/2) which is attributed to PtSe₂; the smaller peaks positioned at $\sim 75.4 \text{ eV}$ and 71.8 eV which can be ascribed to Pt-oxides and unreacted Pt metal, respectively [31]. XPS shows that about 6% amorphous Se, likely due to the excess required for the TAC process. The majority of the Pt is bound to Se, however some oxides and elemental Pt persist. This is consistent with previous XPS studies of PtSe₂ produced by this method [31]. The Se 3d core-level spectrum is composed of two contributions: the dominant one from PtSe₂ at 54.9 eV and the other relatively small one positioned at higher binding energy of 55.8 eV possibly indicative of the amorphous Se [59]. The atomic percentage of Pt and Se in the film contributing to PtSe₂ is determined to be approximately 34.7% and 65.3%, respectively.

STM was used to study the morphology and crystal structure of the polycrystalline TAC-grown PtSe₂ films, which revealed features comparable to those observed in other STM studies [60–62]. The large-scale features visible with scanning electron microscopy (SEM), Fig. 1(f), are also apparent in the STM images of the film, shown in Fig. 1(g). Large-area STM scans show many grains have hexagonal shapes (Figs. 1(g) and S12(a) & (b)). It can also be seen that there is some contamination present on the surface of the sample. The film morphology as observed by STM is consistent with the AFM measurements (Fig. S11). The crystallinity of the PtSe₂ grains and the high density of steps at the crystal edges is further highlighted in Fig. S12(b). It can be seen from the line profiles, Fig. S12(c), that the PtSe₂ crystallites' thickness typically varies from 1 to 6 nm. From atomically-resolved images of the surface, such as Fig. 1(h), an average lattice constant of 0.38 nm is obtained, which matches very well with earlier reports [14,30]. It should be noted that not all sites were defect free, and the observed OD defects, such as those shown in Fig. S13, are consistent with another recent STM study, the contrasting synthesis approaches notwithstanding [14,63]. The STM analysis reveals a number of distinct OD defects on the PtSe₂ film, e.g., at least three OD defects identified under positive and negative bias voltages in the atomically-resolved STM images shown in Fig. S14. From basic STM analysis it can be concluded that the films are edge rich and the polycrystalline grains can reach 40 nm in lateral size, and OD defects are evident in some areas.

In HAADF-STEM images, Figs. 1(i) and S15(a), multilayers with different edge terminations are apparent. HAADF imaging, which is also called Z-contrast imaging, not only allows dynamics and positions of atoms to be monitored but also enables elements to be identified since the HAADF signal increases with the atomic number Z as $Z^{1.64}$ for isolated atoms (the intensity of signal changes almost linearly with the sample thickness) [64]. The termination of edges and the number of PtSe₂ layers can therefore be identified from the HAADF signal intensity.

The brightest atoms on the images correspond to the Pt ($Z = 78$) whereas less bright atoms are Se ($Z = 34$). It should be noted that HAADF is not a spectroscopy mode and the elemental identification of heteroatoms is not possible if the atomic numbers of heteroatoms are close or the specimen imaged is of unknown composition. As the intensity changes linearly with thickness, the number of PtSe₂ layers varies between 1 and 5 in Fig. 1(i) (see also Fig. SI5(b)).

Atomic resolution HAADF-STEM images of monolayer PtSe₂ with two different terminations and the corresponding density functional theory (DFT) relaxed atomistic models are shown in Fig. 2(a–d). Since the edge atoms have fewer neighbors compared to atoms at the smooth

surfaces, the atoms at the edge of TMDs are chemically more active and unstable under electron beam irradiation in STEM [65–67]. Fig. 2(a, b) shows that the Se-terminated (type A configuration) edge of ML PtSe₂ has more atoms compared to the Pt-terminated edge (type B configuration) as shown in the atomistic models in Fig. 2(c, d). The reconstructed type A edge configuration (Fig. 2a) therefore looks complex while the type B configuration (Fig. 2b), with fewer edge atoms, is straight and more stable. The raw HAADF images of type A and B edge configurations are shown in Fig. SI5(c, d). To gain further insight into the atomistic effects of edges in PtSe₂, we investigated the edge structural configurations and electronic properties of PtSe₂ using fully

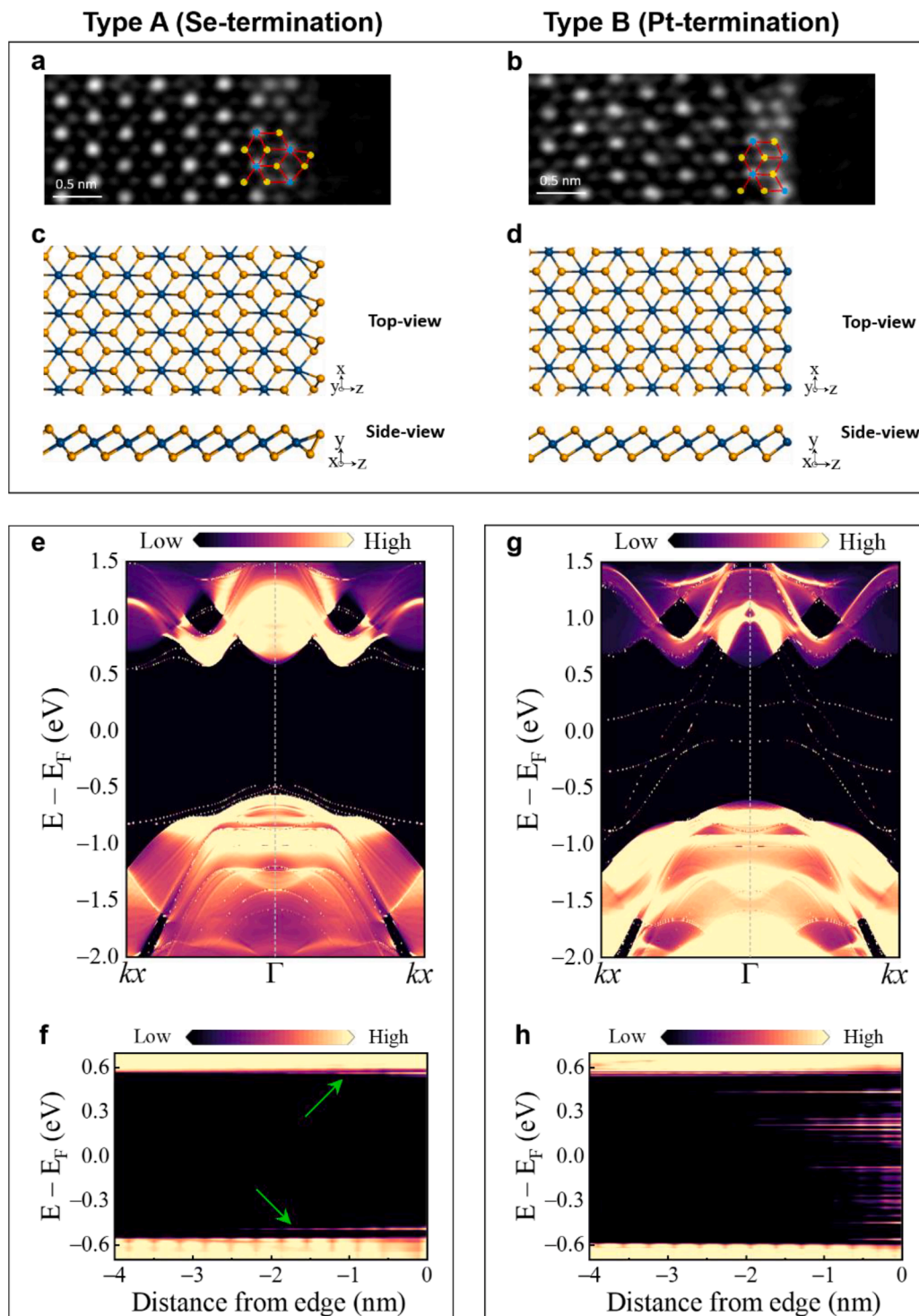


Fig. 2. Different edge configurations and their impact on the electronic structure of ML PtSe₂. Left: type A configuration (Se termination). Right: type B configuration (Pt termination). Filtered atomic-resolution HAADF-STEM images of ML PtSe₂ with Se (a), and Pt (b) terminations. Blue and yellow spheres represent Pt and Se atoms respectively in the model in (a) and (b). The raw HAADF images are shown in Fig. SI5(c,d). Atomic structure post geometry relaxation for Se terminated (c), and Pt terminated edges. (d) PtSe₂ viewed from side and top. (e) Contour plot illustration of the band structure projected at the edge of type A configuration, i.e. on the Se atom at the edge, considering SOC. (f) Magnified LDoS displaying the spatial extent of the edge-induced states in the bandgap in real space along the PtSe₂ ML layer from the edge. (g and h) Edge band structure, projected on the Pt atom at the edge, and magnified LDoS for type B edge. Energy states are created in the bandgap as a result of the edge configuration. The density of these edge-induced states and their energy distribution across the bandgap are more pronounced in type B edge configuration compared to type A (shown by green arrows in (f)).

relativistic first-principles calculations (see Experimental Methods for details). Both type A and type B edge configurations were considered for monolayer (ML) and bilayer (BL) PtSe₂. Shown in Fig. 2(c, d) are the side-view and top-view of the relaxed structures of ML PtSe₂ with Se- and Pt-terminated edge configurations. Although the edge is reconstructed in the type A termination, as can be conspicuously seen in Fig. 2(d), no considerable reconstruction was observed in type B compared to the as-cut structures. Notably, in the type B configuration, there is a contraction of Pt-Se bond lengths ($b_{\text{Pt-Se}}$) at the edge from 2.54 Å to 2.42 Å while in type A there is a slight increase of $b_{\text{Pt-Se}}$ from 2.54 Å to 2.57 Å.

A third zigzag termination is also possible (type C). The atomic structures before and after geometry optimization are shown in Fig. S16. As can be seen, no significant reconstruction is observed at the edge. The surface band structure including spin orbit coupling (SOC) is also included in the SI, Fig. S18. Including SOC in calculations for this edge configuration (type C) leads to distinguishable spin splitting of the edge states, but not as pronounced as for type B edge states. It also leads to slight bandgap opening while calculations without SOC show metallic edge states. Compared to type A, although the edge states do not form a metallic edge, the edge states are distributed across a wider energy band in the bandgap; nonetheless, they do not cross and hence the edge is not metallic.

To investigate the effects of SOC on the edge states, fully relativistic pseudo potentials were used. The electronic structure of the two edge types for the ML PtSe₂ structure is presented in Fig. 2(e–h), demonstrating a bandgap of ~1.2 eV for monolayer PtSe₂, which is consistent with literature values [32,63]. A contour plot of the band structure projected on the Se atom at the edge considering SOC is shown in Fig. 2(e). The edge-induced states are localized at energies close to the conduction band minimum and valence band maximum with spatial distribution of ~2.5 nm from the edge of the structure, as identified in the LDoS at the edge of the structure (green arrows in Fig. 2(f)). As depicted in Fig. 2(g), which demonstrates the edge-band structure projected on the Pt atom at the edge, a large density of edge-induced energy states is created throughout the bandgap at the edge of the Pt-terminated ML PtSe₂ both above and below the Fermi energy (E_F), closing the bandgap and making the edge metallic. Similar to type A configuration, edge states in type B also extend ~2–2.5 nm from the edge (Fig. 2(h)). Such spatially and spectrally localized edge states could contribute to percolated conduction through the PtSe₂ crystallite edges in a polycrystalline film and be a contributing factor to the enhanced edge conduction observed by conductive AFM (see Fig. S19). The presence of edge states is known to similarly impact the electrical performance of MoS₂ crystals [68].

Edge band structures of types A and B edge configurations without SOC are provided in Fig. S110. As can be seen, considering SOC breaks the degeneracy of the edge-induced states created in the bandgap. For the type A edge configuration, zero spin splitting is observed for the edge states at the Γ point while they develop a relatively small spin splitting at wavevectors away from the Γ point. For the type B edge configuration, on the other hand, the metallic edge states (shown by dashed lines in Fig. S110(c) and labeled a and b) demonstrate significantly large splitting, i.e., the **a** and **b** energy bands split into **a'** and **a''**, and **b'** and **b''** bands when SOC is considered. Maximum splitting of the **a** band occurs at an off- Γ wavevector with the value of $\Delta E_a = 385$ meV while it reduces to 320 meV at the Γ point. The **b** band exhibits splitting with a maximum value of $\Delta E_b = 450$ meV at kx (kx represents the edge of the Brillouin zone in the x -direction where the PtSe₂ structure is periodic). Moreover, the spin magnetic moment for type-B configuration at the Pt edge site is $0.39 \mu_B$ (Bohr magneton), which is strongly localized at the edge and diminishes quickly with distance from the step edge (450% reduction in ~0.3 nm from the edge, i.e., edge-state magnetism), while for type-A it is $0.003 \mu_B$. This sizable spin magnetic moment for the Pt-terminated edge is comparable to the reported values for graphene and black phosphorus [69,70]. Such large spin splitting of the energy bands and magnetic moments of the PtSe₂ edge structure would be very promising for

spintronics applications. The edge band structure of type C configuration (single Se-termination) of ML PtSe₂ is shown in Fig. S18. A bandgap of ~130 meV was determined for this type of edge when SOC was considered, which is in agreement with literature [63].

The edge band structure and LDoS of BL PtSe₂ is presented in Fig. S111. This figure demonstrates that no states are created in the bandgap for the type A edge (Se-terminated) configuration, while similar to ML PtSe₂, but type B creates a large density of metallic states across the bandgap. The relative formation energies of the three types of edge configuration, i.e., type A (Se-terminated, stoichiometric edge), type B (Pt-terminated, non-stoichiometric edge with 2 Se atoms missing) and type C (single Se-terminated, non-stoichiometric edge with 1 Se atom missing) are shown in Fig. S112. As can be seen, the edge formation energy is parameter dependent and our calculated energies versus the Se chemical potential are found to be consistent with the published data; i.e., the edge formation energy of the stoichiometric edge configuration (type A) is independent of the chalcogen (in our case Se) chemical potential while the nonstoichiometric edge terminations (type B and C) are dependent on the chalcogen (in our case Se) chemical potential [63,71,72].

A more detailed experimental examination of the crystallite steps, and an investigation of their localized electronic structures, is desirable for comparison with models and for a better understanding of the film properties. Conductive AFM measurements, Fig. S19, at a set bias revealed larger currents at the step edges, which would suggest current percolates through these pathways [68]. However, it could be argued that the improved conductivity could be attributed to better contact between the edge and tip at these points, as would be expected for an edge-contacted 2D material [27]. A more rigorous approach is to use high-resolution STS, which can provide mapped information on the density of electrons as a function of their energy. As can be seen from Fig. S12, the crystallite with the bilayer step edge where STS was performed is at least 3 layers thick. In STS, variations in the tunneling current with voltage are due to differences in the electronic structure, and a map of the electronic structure can be built up by sweeping the bias in a systematic site-by-site manner [73,74]. An STM/STS investigation of the step edges reveals the existence of distinct edge states. Fig. 3(a) shows an STM image of a PtSe₂ bilayer step edge (~1.2 nm) and the grid spectroscopy measurements performed on the step within this area. 50×50 individual $I(V)$ measurements were performed in a grid with dimensions of $10 \times 10 \text{ nm}^2$ (Fig. S113(b, c)). The voltage was swept between ± 1 V. Fig. 3(b) depicts the dI/dV map at electron energy value of -0.2 V, which corresponds to the filled states. Brighter points on the grid correspond to a higher DoS at -0.2 eV. This dI/dV map is characterized by bright lines corresponding to the terrace edge of the PtSe₂. It is interesting to note that the step edge exhibits a higher DoS compared with the flat terraces. These altered electronic properties at the step edge resemble ripples gradually reducing in amplitude further away from the edge. This is understood in terms of edge states, which have different electronic properties from the flat surface. It can be seen that there are states on the lower terrace at certain energies (Fig. S113(d)) which is in line with our DFT calculations for a single layer step presented in Fig. S114. Corresponding current (I/V) maps of the occupied and empty states, taken at bias voltages of around -0.3 and 0.24 V, are shown in Fig. S113(b, c). Fig. 3(c) displays dI/dV plots (numerical derivative of $I(V)$) spectra of three points indicated on the grid by the colored stars, positioned on the flat terraces below and above the step, and at the step edge. It can be seen that the LDoS increases at the edge of the step in comparison with the flat terraces. The LDoS of the filled states at the step edge is several times larger than that measured on the pristine terraces. However, the LDoS of the empty states at the step edge differs only slightly from that of the pristine terraces, except in the window from 0.16 to 0.47 V above the Fermi level where it is slightly lower [19]. From the extended dI/dV plot in the log scale in Fig. S113(d), which also compares lower side of the step edge, it can be observed that there is no band gap but rather a reduced density of states. The spatial distribution

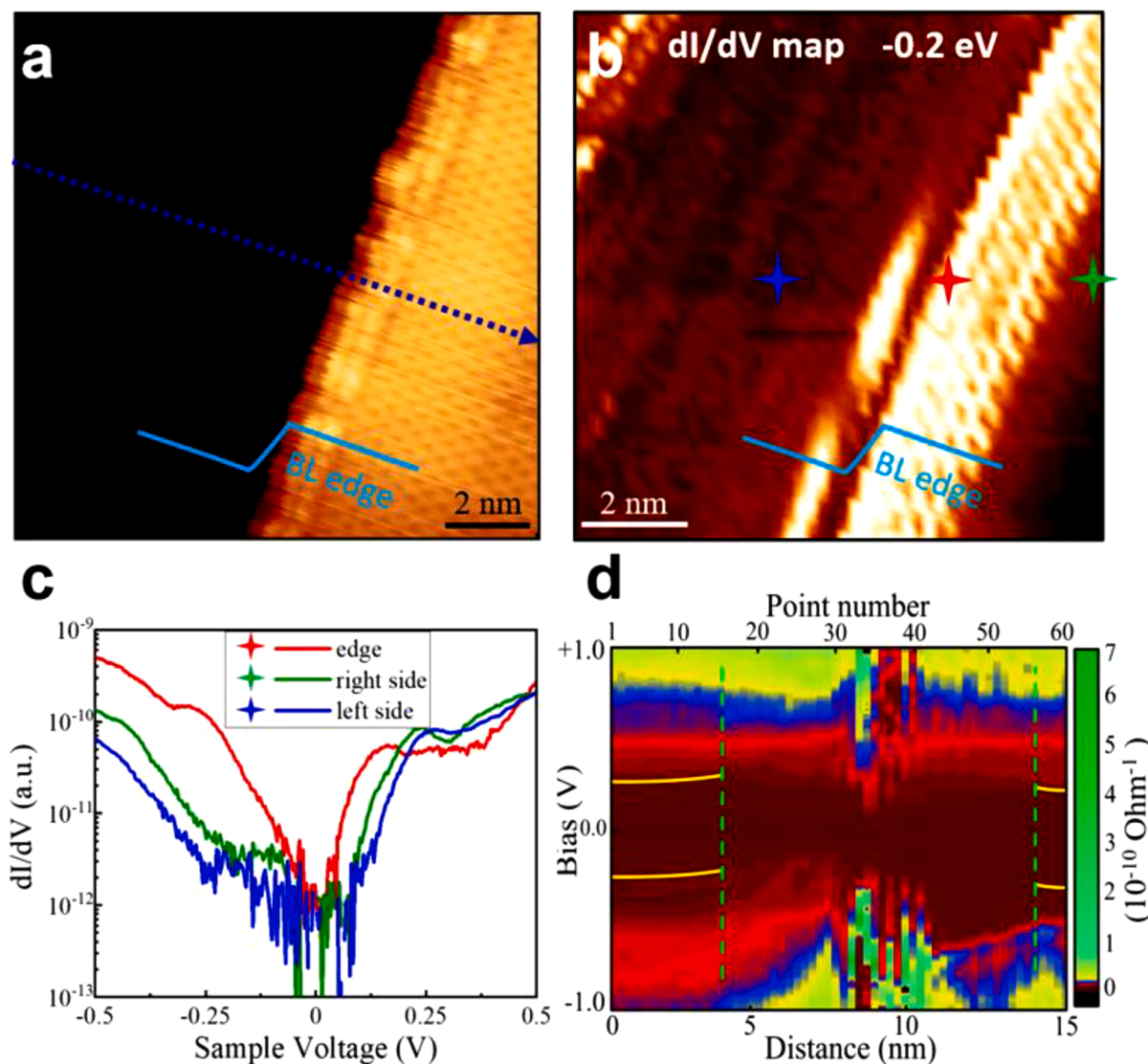


Fig. 3. Grid spectroscopy and band alignment at the edge of PtSe₂. (a) STM topography of bilayer step edge on multilayer PtSe₂ crystallite ($13 \times 13 \text{ nm}^2$, $V = 0.8 \text{ V}$ and $I = 100 \text{ pA}$). Grid spectroscopy ($10 \times 10 \text{ nm}^2$) was performed within the area in (a). (b) Grid dI/dV mapping (50×50 points) at electron energy value of -0.2 eV . Bright lines (higher DoS) correspond to the edge states of PtSe₂ terrace exhibiting ripples gradually reducing in amplitude further away from the edge (stabilization parameters $V = 0.8 \text{ V}$ and $I = 100 \text{ pA}$). (c) Representative dI/dV spectra of three points in the grid indicated by the colored stars (blue, green and red) in (b). (d) 2D plot of tunneling spectra (dI/dV in bias range $\pm 1.0 \text{ V}$) across the blue dashed arrow in (a), demonstrating an increase of the LDOS on the edge of the bilayer PtSe₂ step. The lines are included to illustrate the trend.

of the edge states was obtained by dI/dV mapping. Fig. SI13(e–h) correspond to the dI/dV mapping at electron energies $E = -0.4, -0.2, 0.2$ and 0.4 eV , respectively. Note that since the imaged areas are more than 3 layers of PtSe₂ there is no band gap, which is in agreement with other recent studies [33,63]. Therefore, here the apparent band bending corresponds to the localized transition between low and high DoS. To provide insight into this observation, we constructed an atomistic model of a bilayer-monolayer (BL-ML) PtSe₂ structure with a Pt-terminated edge. A Pt-terminated edge was considered since it was expected to generate a large DoS localized at the edge (see Fig. 2(g and h)), consistent with our additional observation of high conductivity at the flake edges (see Fig. SI9(b)). Although the experimental measurements are for a thicker bulk-like semimetallic structure and the calculated BL-ML structure is semiconducting, both compare favorably and demonstrate the presence of edge states. In other words, the features associated with the ‘transition region’ at the step-edge are broadly consistent for both the experimental (Fig. 3(d)) and the simulated (Fig. SI14(a)) results. Moreover, the overall trend of the band bending

(Fig. 3(d)), which would be attributed to the localized transition between low and high DoS at regions away from the step-edge, is in good agreement with the simulation results (Fig. SI14(b)). The projected density of states (PDOS) at the BL-ML junction obtained within the framework of DFT and non-equilibrium Green’s functions (DFT+NEGF), see Experimental Methods for details, is presented in Fig. SI14(a) for energies above the Fermi level. Each ribbon illustrates the atom-resolved PDOS at individual layers over a distance of $\sim 1.65 \text{ nm}$ at either side of the edge. The inset shows PDOS at a given energy of $E - E_F = 0.53 \text{ eV}$ which demonstrates similar ripples in the DoS to those measured experimentally. A similar characteristic is also observed at energies in the valence band (see Fig. SI14(c)).

The band profile of the PtSe₂ across the step edge was measured by line STS. Spectra were measured every 0.25 nm along the blue dashed arrow (15 nm) in Fig. 3(a). The resulting dI/dV data is plotted in Fig. 3(d) as a 2D map [75,76]. Each vertical band charts dI/dV between -1.0 V and $+1.0 \text{ V}$ of a point along the line, with the magnitude of dI/dV expressed using a color intensity scale. The 2D map shows the evolution

of the edge states on this line. It should be noted that the line- and grid-STs measurements were performed on the atomically resolved and defect-free step (see Fig. 3(a)). An increase of the LDoS near the Fermi level is observed at the bilayer step edge. Although these experimental measurements are for a ~ 1.1 nm step, corresponding to a bilayer, and on a semimetallic film a few nanometers thick, a similar trend in band bending was obtained for the BL-ML heterojunction simulations despite these fundamental differences, as shown in Fig. S114(b). This would appear to suggest that such features at step edges are an intrinsic feature for PtSe₂ even for non-ideal conditions as represented by a polycrystalline film. The upward conduction and valence band bending at both sides of the edge adjacent to the interface is also consistent with previous reports [19,75]. Notably, a localized potential barrier in the conduction band is formed at the BL-ML interface which is expected to play a crucial role in carrier transport across the heterojunction. Moreover, a ~ 2.5 nm transition region can be identified, shown by the vertical dashed lines (see Fig. S114(b)). Fig. S115 also demonstrates a rather large valence band offset ($DE_V \approx 0.8$ eV) at the BL-ML interface which is considerably larger than the conduction band counterpart. Asymmetric band offsets have also been reported for ML-BL MoSe₂; [75]. However, the much larger band offsets observed in the ML-BL PtSe₂ could be attributed to the stronger interlayer interaction in PtSe₂ [32].

To investigate the properties of the monomaterial BL-ML heterojunction for potential applications in the next generation of FETs, the BL-ML edge region was considered as the channel of a back-gated FET. Performance of the BL-ML monomaterial heterojunction is compared with that of a uniformly-thick ML structure as a reference back-gated FET device. A detailed description is provided in the Experimental Methods. The atomic structure of the back-gated devices is illustrated in Fig. 4(a).

To obtain the back-gated FET transfer characteristic, the gate-source bias (V_{GS}) is swept at a given drain-source bias (V_{DS}) – see Experimental Methods for further details. The I_{DS} - V_{GS} characteristic at $V_{DS} = 0.2$ V and at different V_{GS} values for both the BL-ML heterojunction and the reference ML device are shown in Fig. 4(b). Both devices demonstrate

large ON/OFF current ratios, with nearly 3 orders of magnitude enhancement for the BL-ML device. The ML device demonstrates a subthreshold slope of 102.5 mV/decade, while the BL-ML device exhibits a remarkably low subthreshold slope of 64.3 mV/decade which is very close to the 60 mV/decade limit required for low-power applications.

The impact of the gate field on the band alignment at the BL-ML interface, illustrated in Fig. 4(c) in the ON-state ($V_{GS} = 0.3$ V), is expected to play an important role in the current conduction. The gate electrode itself shifts the energies to lower levels compared to a structure with no gate – see Fig. S116. Applying a positive gate field further shifts the energies to lower levels leading to the total collapse of the band offset at the BL-ML interface, causing the conduction band edge of the ML-side to align with the conduction band edge of the BL-side of the junction in the gated region. This coupling of the conduction band states at the BL and ML sides, which could be strengthened by the edge states, creates a transport path for electrons, as depicted by the energy-resolved current in the ON-state in Fig. 4(d). However, this is not the case for the ML device, where the ML bandgap blocks the carrier transport for energies just below source chemical potential, limiting the carrier transport path (see magnified plots in Fig. S117). As a result, and by comparing LDoS and energy-resolved current plots of the ML and BL-ML devices in the ON-state in Fig. S117, the effective energy window between the source and drain chemical potentials ($V_{DS} = 0.2$ V), through which electrons can flow, is wider for the BL-ML structure ($DE = 330$ meV) than the ML structure ($DE = 220$ meV). This leads to the ~ 3 times larger ON-state current of the BL-ML structure compared to the ML device observed in the transfer characteristic in Fig. 4(b).

The LDoS and energy-resolved current of the BL-ML structure in the OFF-state ($V_{GS} = -0.6$ V), presented in Fig. 4(e, f), reveal that the carrier transport mechanism in this state is due to the tunneling through the ML region, i.e., the tunneling length is equal to the length of the ML side of the channel. Hence, to allow a fair comparison between the performance of the two device architectures, the tunneling length of the uniformly-thick ML device was set to equal the tunneling length of the BL-ML

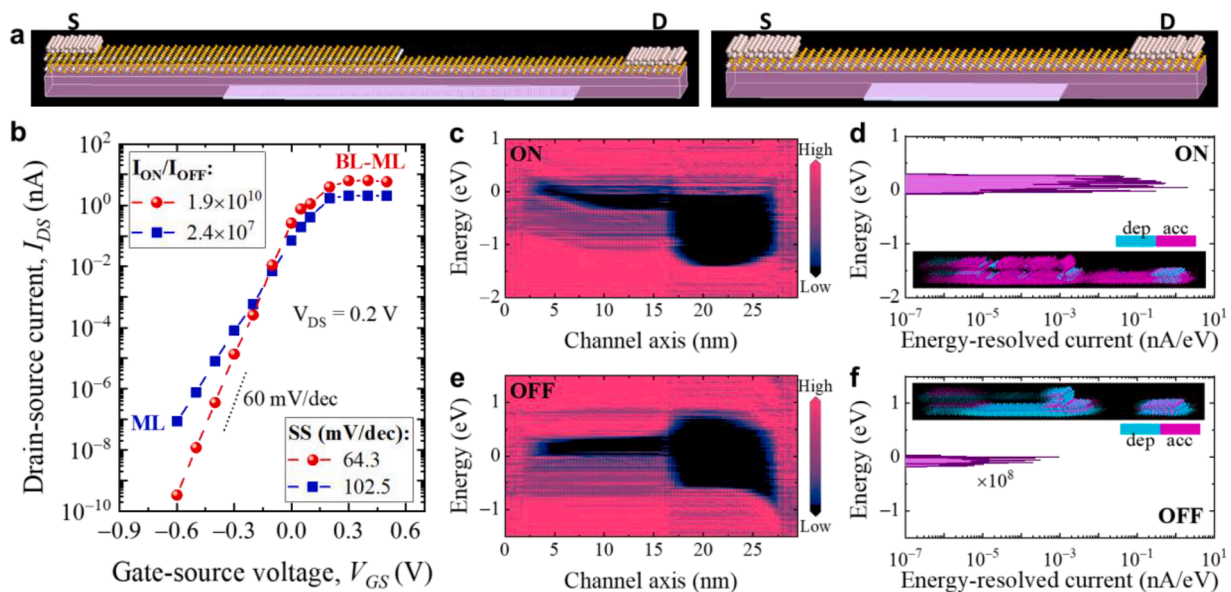


Fig. 4. Back-gated field effect transistors (FETs) and their properties. (a) Atomic structure of FET devices with BL-ML PtSe₂ (left) and uniformly-thick ML PtSe₂ (right) as device channel. (b) Transfer characteristic of the simulated devices, comparing the performance of the BL-ML FET of the edge, with ML FET, at $V_{DS} = 0.2$ V. BL-ML device demonstrates $> 10^{10}$ I_{ON}/I_{OFF} ratio, with subthreshold slope (SS) of just above the 60 mV/decade limit. Notably, the ML device exhibits more than 2 orders of magnitude larger OFF current compared to the BL-ML FETs. (c) LDoS of the BL-ML device at the ON bias condition revealing the impact of the gate field on collapsing the potential barrier at the BL-ML interface leading to the large ON current. (d) Energy-resolved current of the BL-ML device at the ON bias condition demonstrating the effective energy states contributing to the carrier transport from the source to drain in the ON-state. (e) LDoS in the OFF-state which suggests the OFF current mechanism to be tunneling through the ML region. (f) Energy-resolved current in the OFF-state. Insets of parts (d) and (f) illustrate the charge difference density of the BL-ML device in the ON and OFF-states where blue (pink) represents depletion (accumulation) of electrons.

structure (see Fig. S118). The significantly reduced OFF current (> 2.5 orders of magnitude) of the BL-ML device compared to the ML device is identified to be due to: (i) less density of states at the BL-side (being a semiconductor) of the tunneling region in the BL-ML device as opposed to the metallic electrode in the ML device, resulting in significantly suppressed electron transmission, and (ii) bandgap of the BL region blocking almost half of the tunneling energy window in the BL-ML structure (see insets of Fig. S118). Therefore, the two factors involved in the OFF current transport mechanism are not dependent on the edge-induced states at the BL-ML edge.

The charge difference density (CDD) of the BL-ML device in the ON- and OFF-states is illustrated in the insets of Fig. 4(d,f), which demonstrate electron accumulation (depletion) in the ON (OFF) state. More specifically, it is shown that with ON conditions ($V_{GS} = +0.3$ V) the entire channel is accumulated. However, with OFF conditions ($V_{GS} = -0.6$ V) the gated region of the channel, where energy states exist in the V_{DS} bias window (source chemical potential – drain chemical potential = 0.2 V), is depleted. However, BL-ML edge-induced states below the source chemical potential are occupied and hence are not fully depleted, resulting in a slight accumulation at the BL-ML edge and the transition region adjacent to the edge.

This insight into the underlying physics governing the device performance would be useful for device optimization by engineering the dimensions of the gated region and underlap region at the BL-side (source), taking into account the transition region at the BL-ML interface to further enhance the ON current and hence the device performance.

3. Discussion

The edge-rich nature of TAC-grown polycrystalline PtSe₂ films was studied by a combination of microscopy methods including SEM, STEM, AFM and STM. The energy profile of step edges was investigated and the origin of edge-induced states observed by STS mapping were explored through theoretical surface Green's function methods, supported by atomically resolved STEM imaging. Although environmental factors may also influence the properties of the step edges, the correlation between experiment and theory indicates the step edge-induced states are a fundamental and crucial feature for PtSe₂, which has to be taken into account for use of such polycrystalline films. Both Pt and Se terminated step edges were found to be present and exhibit distinct properties. Our results reveal that Se-termination of the edges leads to the formation of states at energies near the conduction and valence band edges while Pt-termination of the edges creates energy states throughout the bandgap, making the edge metallic and extending to ~ 2 – 2.5 nm from the edge. Furthermore, our fully relativistic DFT calculations reveal large spin splitting of maximum 450 meV and spin magnetic moment of 0.39 μ_B for the Pt-terminated edge structure indicating its potential for spintronic applications. Due to the strong interlayer interaction and thickness-dependent bandgap in PtSe₂, a ML-BL structure can form a lateral monomaterial heterojunction demonstrating straddling type band alignment. The role of the edge-induced energy states and their impact on the carrier transport through the step edge, explored through a concept FET, consisting of the lateral ML-BL heterojunction as the channel. The combined impacts of the suppressed DoS in the BL side and the tunneling distance in the OFF-state, lead to a $> 10^{10}$ I_{ON}/I_{OFF} ratio and ~ 64 mV/dec subthreshold slope. We believe that this study provides insight into the electrical behavior of large-area back-end-of-line compatible grown polycrystalline films and the device concept offers exciting opportunities to further improve the performance of TMD-based electronic devices.

4. Experimental methods

4.1. Synthesis and transfer of PtSe₂

Pt films of 5 nm were deposited on SiO₂/Si substrates by argon-ion sputtering (Gatan Precision Etching and Coating System (PECS)). PtSe₂ was obtained by annealing the samples at 450 °C for two hours under a 150 sccm H₂/Ar flow, in the presence of an upstream Se metal source heated to approximately 250 °C [31,32]

For conductive measurements the PtSe₂ was liberated from the growth substrates by wet etching, as noted in Fig. 1(b). Conductive substrates were prepared by depositing Au/Ti (35 nm/5 nm) onto a SiO₂/Si substrates using a Temescal FC2000 electron-beam evaporation system. The PtSe₂/SiO₂/Si samples were floated on a 2 molar NaOH solution. When the substrate had detached from the surface film, the floating films were fished from the solution with a glass slide and refloated in de-ionized water. The films were then lifted out on the prepared conductive substrates, rinsed with deionized water, and dried. For the films with an initial 5 nm Pt thickness, it was possible to transfer without the aid of PMMA, reducing contamination for STM studies. Spin-coated PMMA is required to support thinner films during the transfer process, after transfer and drying the supporting PMMA was dissolved by acetone and the films were washed in isopropyl alcohol.

4.2. Sample characterization

A low-temperature STM from Createc was used for all STM/STS experiments. The experiments were carried out under ultra-high vacuum (UHV) conditions with a base pressure $\sim 3 \times 10^{-11}$ mbar. All the STM images were recorded at 77 K in constant current mode (CCM). The preparation chamber of the STM is fitted with a heating stage for annealing the samples. Electrochemically-etched (001)-oriented single-crystalline tungsten tips in NaOH were cleaned *in situ* with argon-ion bombardment. The bias was applied to the sample with respect to the tip. It was possible to image the surface without heat treatment in vacuum. However, since the sample was wet transferred using an NaOH etching solution which might influence the edge structures, to reduce possible contamination and to improve the quality of the obtained images the sample was subject to a mild annealing, up to 220 °C for 7 hours in UHV, similar to procedures used elsewhere [33,63]. For STS, at each point the spectra were averaged from 15 I(V)-data. The STM tip height was stabilized by the CCM scanning parameters when it was moved across the grid. The CCM scanning parameters used to move between points were $V = 0.8$ V and $I = 100$ pA. The corresponding dI/dV maps in this work were obtained by numerical differentiation of plots extracted from the I-V map that was measured by grid spectroscopy.

XPS spectra of the Pt 4f and Se 3d core-levels were measured utilizing a monochromated Omicron MultiProbe XPS system with an aluminum X-ray radiation source (1486.7 eV) and instrumental resolution of 0.6 eV under UHV conditions (5×10^{-11} mbar). After subtraction of a Shirley background, the core-level spectra were fitted with a combination of Gaussian-Lorentzian line shapes using the CasaXPS software.

STEM imaging was performed using a Nion UltraSTEM100 operated at a 60 kV accelerating voltage in near-UHV ($\sim 10^{-9}$ mbar) using a HAADF detector with a collection angle of 80–300 mrad. The convergence semiangle of the electron probe was 30 mrad. The background of HAADF images shown in Fig. 2(a, b) was removed by subtracting a Gaussian blurred (radius 20 px) copy of the image. These HAADF images were further processed using double Gaussian filtering [64], and Wiener filtering [77], in order to reduce noise and increase contrast of the images. SEM images were acquired using a Karl Zeiss Supra microscope operating at 3 kV accelerating voltage with a working distance of ~ 3 – 4 mm and a 30 μ m aperture.

AFM was performed with a Bruker Multimode 8 in PF-TUNA mode using Adama Innovations Super Sharp boron doped conductive diamond coated probes (model: AD-2.8-SS).

Raman spectra were gathered using a WITec Alpha 300 R confocal Raman microscope at $100\times$ magnification with a 532 nm excitation source and a spectral grating of 1800 lines/mm.

4.3. Density functional theory

The edge structural configurations were obtained and the electronic structure calculations were performed within the framework of Kohn-Sham DFT and surface Green's function (SGF), as implemented in QuantumATK package [78,79]. In the SGF calculations, periodic boundary condition was applied only along the directions parallel to the edge plane. In order to study a realistic single-edge structure similar to an experimental one, as opposed to a conventionally used double-edge in a slab model, a recently developed SGF method has been employed in our calculations. This method allows a semi-infinite structure to be considered enabling charge transfer between the bulk and the edge regions, self-consistently, in a full quantum-mechanical treatment. The norm-conserving pseudopotentials and pseudo-atomic localized basis were used with Perdew-Burke-Ernzerhof (PBE) of generalized gradient approximation (GGA) to describe the exchange correlation energy [80]. OpenMX numerical atomic orbital basis sets s4p3d2 and s4p3d3 were used for Pt and Se, respectively [81]. Geometry optimization was performed using LBFGS (low-memory BFGS) algorithm. Moreover, in the cell optimization the Bravais lattice type was kept constant. Structural optimization was achieved by minimizing the total energy with respect to the atomic positions until the maximum force component became less than 0.01 eV/Å. Monkhorst-Pack scheme [82]. for Brillouin-zone integrations with a density of approximately 10 k-points per angstrom has been considered. An energy cut-off of 120 Ha and energy convergence criterion of 10^{-7} Ha have also been considered. For discretized grid, ~ 520 k-point per angstrom have been used for the SGF calculations, in the direction normal to the edge plane. We have incorporated van-der-Waals (vdW) interaction for bilayer PtSe₂ structures by adding a nonlocal vdW term to the local and the semi-local exchange correlation functionals utilizing Grimme's dispersion correction [83].

To describe the single edge of the PtSe₂ monolayer and bilayer, a surface configuration consisting of an electrode and a central region were considered, where the electrode describes the bulk properties of the system and the central region provides the effects of the edge states. Green's function couples the central region Hamiltonian to semi-infinite monolayer or bilayer PtSe₂ with periodic boundary condition along x- and y-directions and with open boundary condition along out-of-edge-plane direction (z-direction) - see Fig. 2(c, d). Self-energy matrix describes the semi-infinite electrode which is created by a recursive method [79]. For type A configuration, 110 (300) Å was considered for ML (BL), and for type B, 200 (300) Å was considered for ML (BL) as the length of the central region to converge the electrostatic potential with respect to the periodic potential of the electrode (see Fig. SI19).

To obtain the band alignment at the BL-ML heterojunction, non-equilibrium quantum transport calculations were performed based on first-principles calculations. DFT together with the non-equilibrium Green's functions (NEGF) technique (DFT+NEGF) was utilized to represent realistic open systems to determine the coherent transport properties of the system fully self-consistently [84]. For discretized grid, ~ 520 k-point per angstrom have been used for the NEGF calculations, in the channel axis, i.e., z-direction.

To investigate the effects of SOC on the edge states, the electronic structure calculations were performed with and without the consideration of SOC using fully relativistic pseudo potentials.

4.4. FET model

The simulation cell of the heterojunction in the BL-ML FET structure has a cross sectional area of $0.376 \times 4 \text{ nm}^2$ parallel to the junction, i.e., (x,y) plane, where the 0.376 nm is the repeat unit in the plane of the film (x-direction) and the 4 nm is the repeat unit normal to the film (y-

direction), considering the periodic boundary condition in the (x,y) plane. The length normal to the junction (z-direction) is 29.56 nm with 16.45 nm bilayer and 13.11 nm monolayer PtSe₂, including the ~ 2 nm source and drain regions under the Al contacts. Al metal was considered as the source and drain electrodes and the gate-oxide was described through electrostatic coupling of a continuum with hafnium oxide dielectric constant of $\epsilon_{\text{HfO}_2} = 25$ and thickness of 1 nm [85,86]. The gate length was 15 nm for the BL-ML device, centered at the step edge, and 7.5 nm for the ML device. Since the simulations and experiments have shown that the edge-induced states propagate further into the thicker side of the BL-ML junction, a longer BL region was considered to allow all possible effects to be accommodated. This configuration resulted in asymmetric underlap regions in the BL-ML junction channel, i.e., the underlap region is ~ 6 nm and ~ 2.7 nm at the BL (source) and ML (drain) sides of the BL-ML channel respectively, while it is ~ 2.7 nm in both source and drain sides of the ML channel.

To capture the performance of the back-gated FETs, the DFT+NEGF approach was employed [87,88]. Semi-infinite electrodes, which consist of the Al contact and the underlying PtSe₂ region, were described by the self-energies defined in the DFT+NEGF framework. OpenMX numerical atomic orbital basis sets s4p4d2 were used for Al atoms. In the device transfer characteristics calculation, the electrode conditions were achieved by holding the source and drain regions at thermal equilibrium described by the Fermi-Dirac distributions at $T = 300$ K, and by considering chemical potential difference $\mu_{\text{DS}} = qV_{\text{DS}}$ for the drain-source bias. Electron transmission under each bias condition ($T(E, V_{\text{DS}}, V_{\text{GS}})$) was calculated self-consistently in the DFT+NEGF approach. Upon obtaining the electron transmission, the drain-source current (I_{DS}) was calculated using the Landauer-Büttiker formula [89]:

$$I_{\text{DS}} = \int i(E)dE,$$

$$i(E) = \frac{e}{h} T(E, V_{\text{DS}}, V_{\text{GS}}) [f_{\text{S}}(E) - f_{\text{D}}(E)]$$

where E, e, and h are energy, electron charge, and Planck's constant, respectively. T is transmission, and $f_{\text{S}}(E)$ and $f_{\text{D}}(E)$ are the Fermi-Dirac distribution functions at the source and drain electrodes, respectively.

Subthreshold slope (SS), which is defined as the inverse of the derivative of log of drain current with respect to the gate voltage, i.e., $SS = \left(\frac{d(\log_{10}(I_{\text{DS}}))}{dV_{\text{GS}}} \right)^{-1}$, is a measure of the electrostatic control of gate (V_{GS}) over the drain-source current (I_{DS}) in a field effect transistor.

Author contributions

The manuscript was written through contributions of all authors. L. A. and F.G. conducted the theoretical calculations and atomic-scale simulations. N.M. and C.Ó. synthesized PtSe₂ films and performed Raman, SEM, AFM characterization and analysis. K.Z. performed STM and STS, and analysis. A.Z. performed XPS and helped with characterization and analysis. K.E. and J.K. performed TEM and analysis. K.Z., L. A. N.M. C.Ó. and F.G. conceived and designed the experiments and wrote the manuscript. I.V.S, P.K.H. and G.S.D. helped with the planning of the experiments, interpretation of the results, and supervision of the project. All authors discussed the results and reviewed the manuscript.

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review & editing. **Jani Kotakoski:** Investigation, Data curation, Visualization, Writing – review & editing. **Igor V. Shvets:** Conceptualization, Resources, Supervision, Writing – review & editing, Funding acquisition. **Georg S. Duesberg:** Conceptualization, Resources, Supervision, Writing – review & editing, Funding acquisition. **Paul K. Hurley:** Conceptualization, Resources, Supervision, Writing – review & editing, Funding acquisition. **Niall McEvoy:** Methodology, Investigation, Data curation, Visualization, Resources, Writing – original draft, Writing – review & editing. **Cormac Ó Coileáin:** Conceptualization, Methodology, Investigation, Data curation, Visualization, Writing – original draft, Writing – review & editing. **Farzan Gity:** Conceptualization, Methodology, Formal analysis, Investigation, Data curation, Visualization, Writing – original draft, Writing – review & editing.

Declaration of Competing Interest

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper

Data availability

Data will be made available on request.

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Supplementary materials

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